## **AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Original) An image capturing apparatus comprising:

a solid image pickup element including a photosensitive element and a transistor for logarithmic transformation, in which an output signal from the photosensitive element is input into a first electrode, for outputting a signal, which is logarithmically varied with respect to an incident light intensity to the photosensitive element; and

a voltage controller for controlling a voltage to be applied to a second electrode of the transistor,

wherein the voltage controller applies a first reset voltage to the second electrode, so as to reset the transistor in such a manner that the image capturing apparatus is operated in a state of moving object extraction image pickup, and

wherein the first reset voltage is a voltage for resetting the transistor in one reset level selected from a plurality of reset levels of the transistor.

- 2. (Currently Amended) An image capturing apparatus according to claim 1, wherein said voltage controller varies at least one of [[the]] <u>a</u> voltage value and <u>an</u> applying time of the first reset voltage, so as to vary the reset level.
- 3. (Currently Amended) An image capturing apparatus according to claim 1, further emprises comprising a detector for detecting the luminance of the object, wherein said voltage controller varies the reset level according to the object luminance detected by the detector.
- 4. (Original) An image capturing apparatus according to claim 3, wherein said voltage controller decreases the reset level as the object luminance detected by the detector is greater.